7-27-2021

I now believe that if a state diagram is needed, it should be accompanied by a general flowchart. If a general flowchart is not to be included, the state diagram should at least contain contextual information about the circuitry surrounding the chip.

Turns out that the current interrupt register is complete, its just that I wasn’t looking at it carefully enough yesterday.

7-26-2021

I think that using flowcharts may be the most optimal way of fleshing out the design of my computer.  
It is a nice visual way of depicting its operation; something that may be useful when taking breaks from working on this project.   
  
Also, the current interrupt register in the interrupt module does not have any enable pins on it, I need to fix that and create a flow chart to fully render the modules behavior.   
  
I feel like I need to work on the interrupt module a little more, to detail the role of the operating state register and how it interacts with the rest of the module.